

Changmin Shin

Accelerated Intelligent Systems Lab. (AISys) @ Seoul National University

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Research Interest: MEMORY SYSTEMS & ARCHITECTURES

Computer System, Memory Architecture, Processing-in-Memory, Graph Processing, Accelerators, Large Language Models (LLMs)

EDUCATION

- **Seoul National University** Seoul, Korea
Ph.D. Student - Electrical & Computer Engineering *March 2024 -*
- **Seoul National University** Seoul, Korea
B.S. - Electrical & Computer Engineering *March 2020 - February 2024*

PUBLICATIONS

¹: indicates co-first authors.

- **FALA: Locality-Aware PIM-Host Cooperation for Graph Processing with Fine-Grained Column Access:**
Changmin Shin, Jaeyong Song, Seongmin Na, Jun Sung, Hongsun Jang, and Jinho Lee *MICRO*, 2025
- **Piccolo: Large-Scale Graph Processing with Fine-Grained In-Memory Scatter-Gather:**
Changmin Shin, Jaeyong Song, Hongsun Jang, Dogeun Kim, Jun Sung, Taehee Kwon, Jae Hyung Ju, Frank Liu, Yeonkyu Choi, and Jinho Lee *HPCA*, 2025
- **A Case for In-Memory Random Scatter-Gather for Fast Graph Processing:**
Changmin Shin¹, Taehee Kwon¹, Jaeyong Song, Jae Hyung Ju, Fank Liu, and Jinho Lee *IEEE CAL*, 2024
- **INF²: High-Throughput Generative Inference of Large Language Models using Near-Storage Processing:**
Hongsun Jang, Siung Noh, Changmin Shin, Jaewon Jung, Jaeyong Song, Jinho Lee *arXiv*, 2025

HONORS AND AWARDS

- **Outstanding Poster Award @ KIISE Computer System Society Winter Workshop - Feb. 2025**

TEACHING EXPERIENCE

- **Embedded Systems Design.** Seoul National University
Teaching Assistant *Fall 2024*